



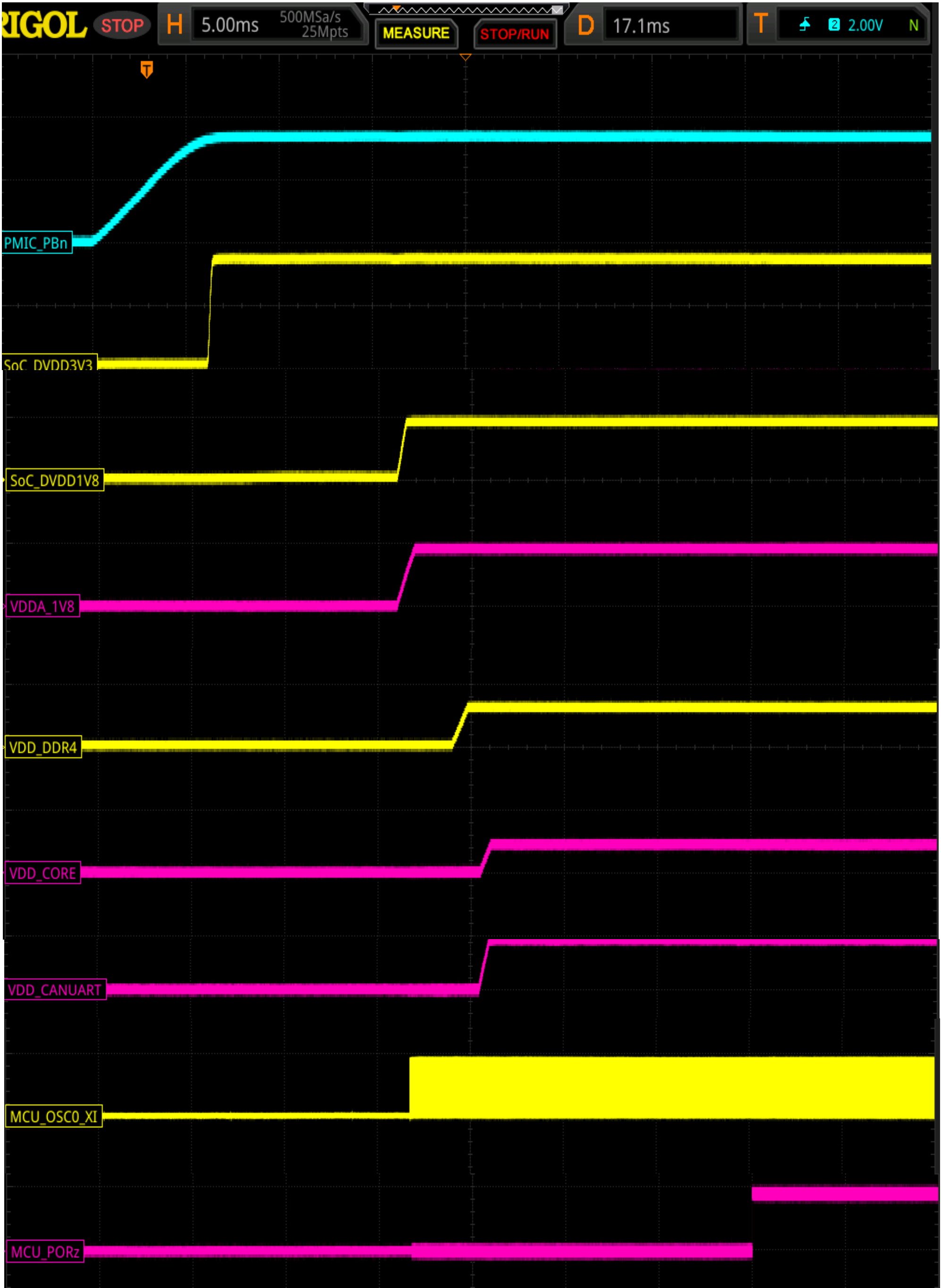
SK-AM62B-P1 SKEVM
REV A
PROC142A
Power up Sequence

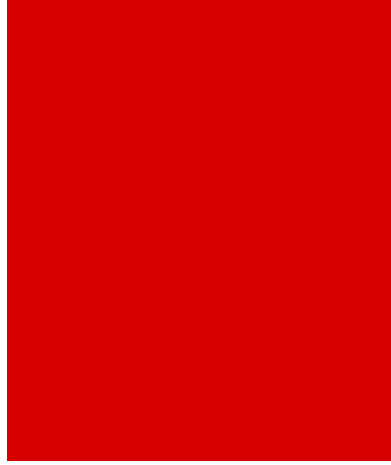
Power Up sequence timing measurements

Power Supply	Voltage (V)	Elapsed Time (mS)
PMIC_PBn	3.3	0
SoC_DVDD3V3	3.3	0.650
SoC_DVDD1V8	1.8	10.05
VDDA_1V8	1.8	10.05
VDD_DDR44	1.2	13
VDD_CORE	0.85	14.3
VDDR_CORE(Sourced from VDD_CORE)	0.85	14.3
VDD_CANUART (Always On)	0.85	14.65
MCU_OSC0_XI	1.8	10.15
MCU_PORz (Reset)	1.8	28.45



Power Up sequence Diagram





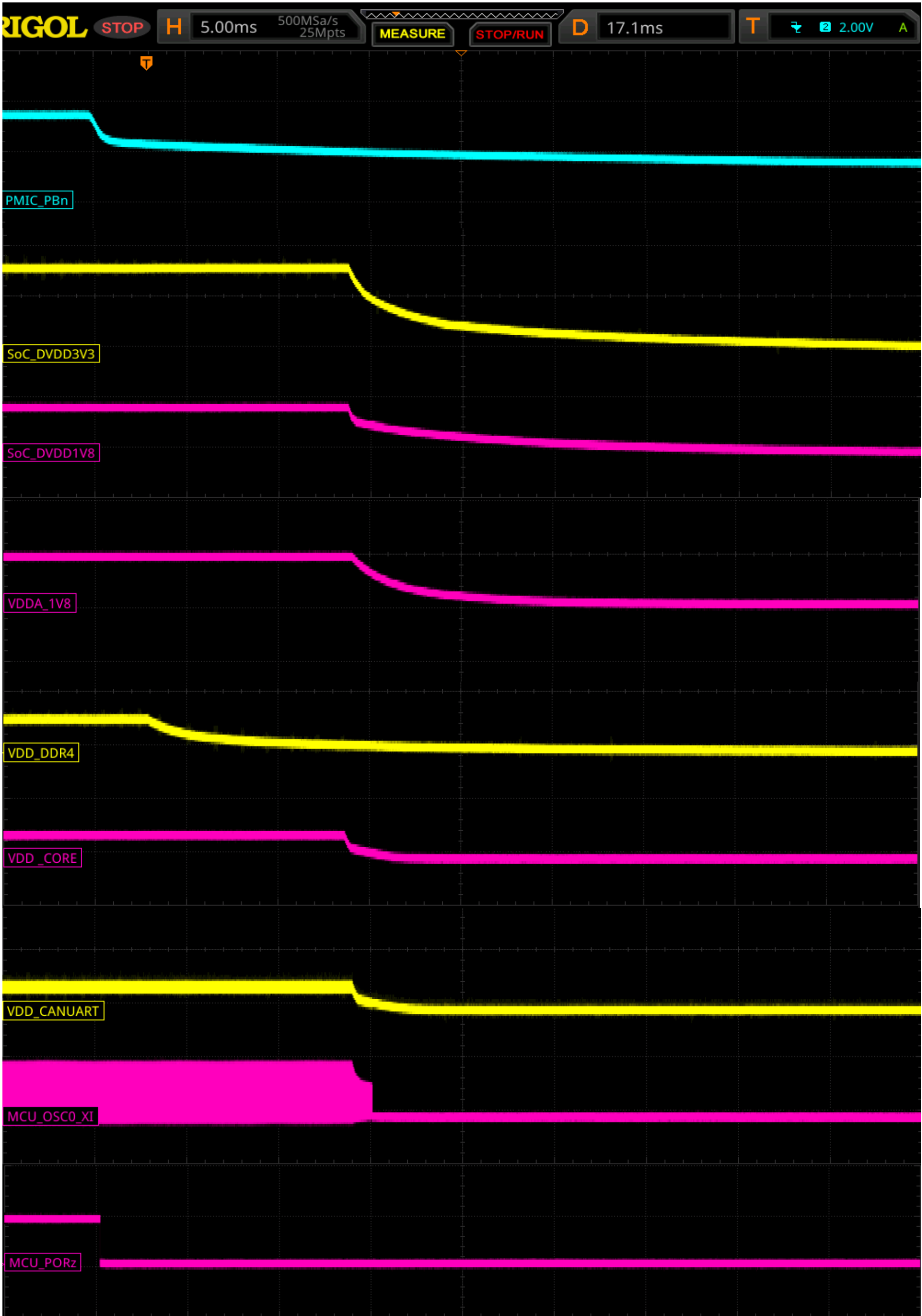
SK-AM62B-P1 SKEVM
REV A
PROC142A
Power Down Sequence

Power Down sequence timing measurements



Power Supply R	Voltage (V)	Elapsed Time (mS)
PMIC_PBn	3.3	0
MCU_PORz (Reset)	3.3	0
SoC_DVDD3V3	3.3	10.75
SoC_DVDD1V8	1.8	10.75
VDDA_1V8	1.8	10.75
VDD_DDR4	1.2	0.3
VDD_CORE	0.85	10.8
VDDR_CORE(Sourced from VDD_CORE)	0.85	10.8
VDD_CANUART (Always On)	0.85	10.8
MCU_OSC0_XI	1.8	10.8

Power Down sequence diagram



Combined Power sequence diagram

